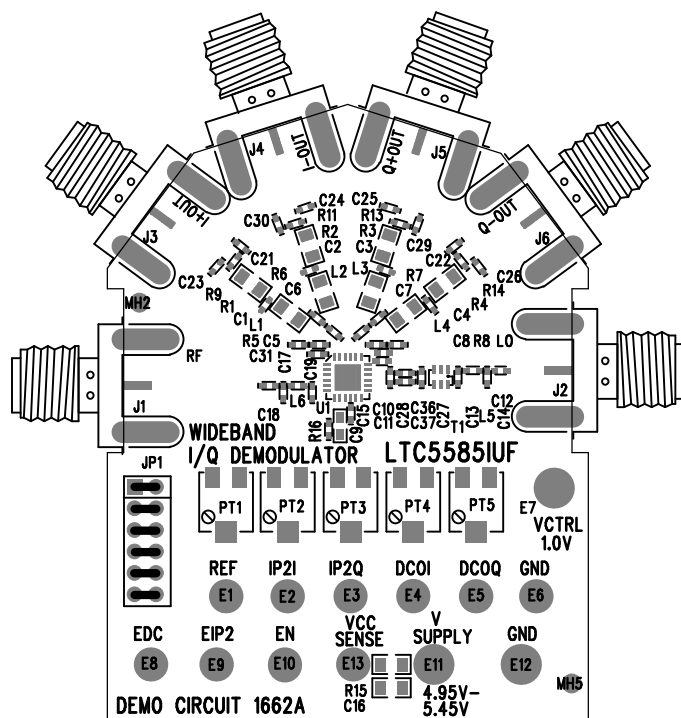
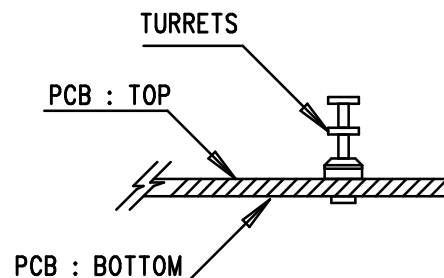


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	SUNNY H.	11-30-11



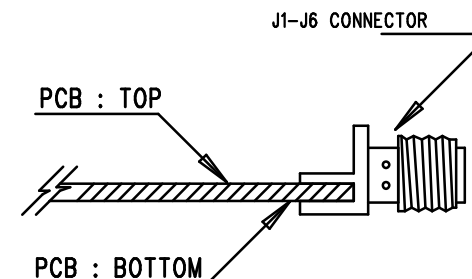
TOP SILKSCREEN
LINEAR TECHNOLOGY

DC1662A-3-LTC5585IUF DATE: 11-30-11
WIDEBAND I/Q DEMODULATOR
With IIP2 and DC Offset Control



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.



APPROVALS

PCB DES.	AK
APP ENG.	SUNNY H.

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING
WIDEBAND I/Q DEMODULATOR
With IIP2 and DC Offset Control

SIZE N/A	IC NO. LTC5585IUF DEMO CIRCUIT 1662A	REV. 3
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SCALE = NONE

FILENAME: DC1662A-3.PCB

SHT 1 OF 1